

FROM EDC知財部

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Watanabe



Fig. 5

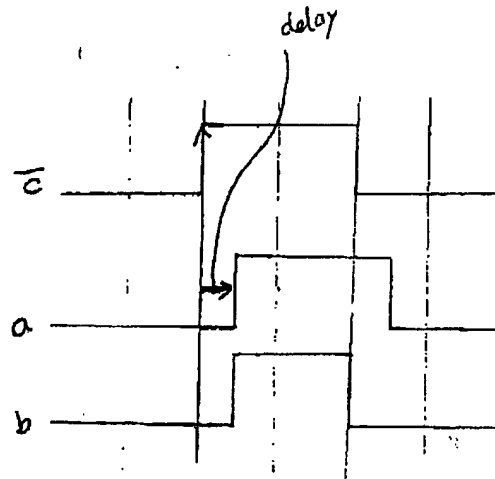
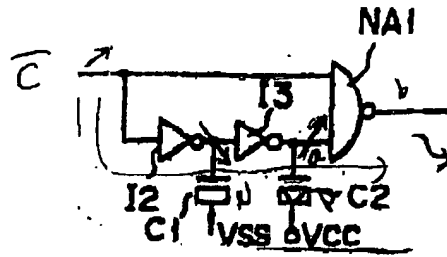


Fig. 7

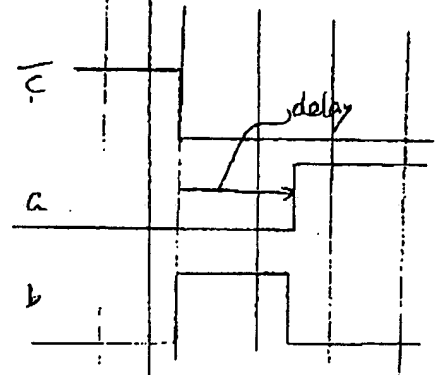
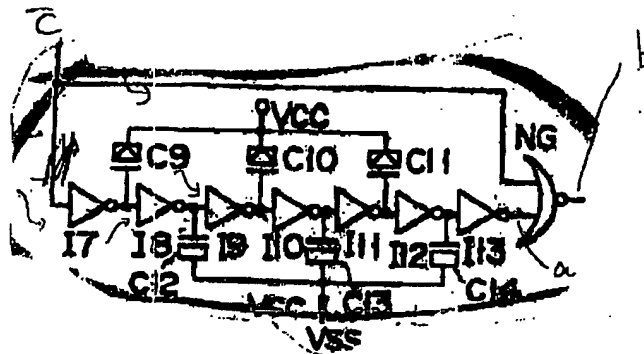
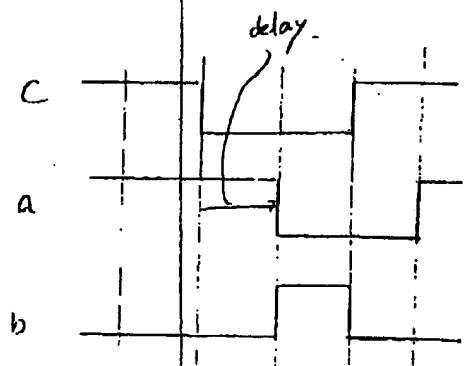
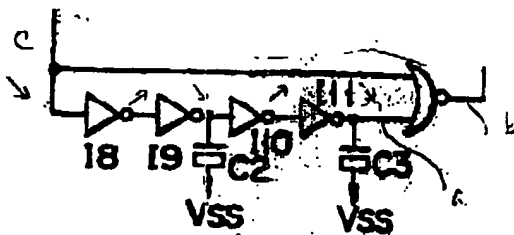


Fig. 11



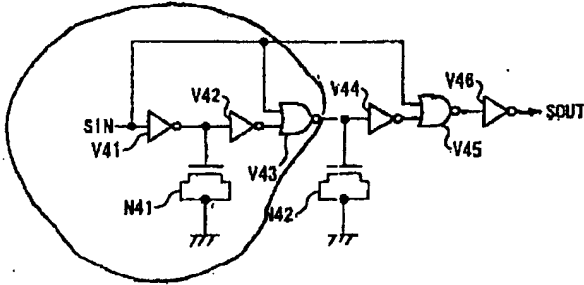
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The present Invent ~

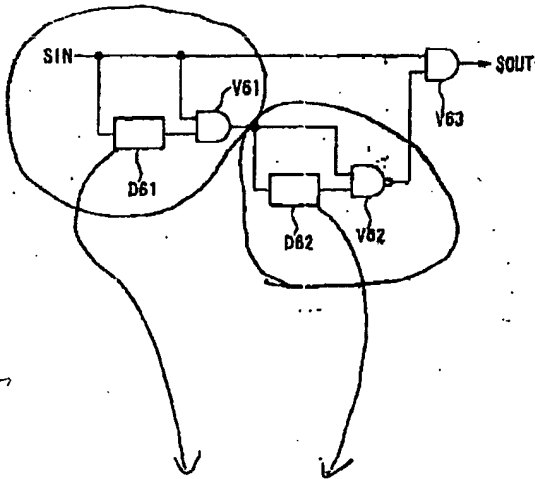


FIG. 4



NOR type

FIG. 6



AND type

NAND type

FIG. 1A

